

CLAIMS:

What is claimed is:

1. An apparatus comprising:
a host controller capable of coupling a plurality of queue heads to a frame list,
wherein the plurality of queue heads are coupled to the frame list before any split-isochronous transaction descriptors where split-isochronous transaction descriptors are supported.
2. The apparatus of claim 1, further including a host controller driver.
3. The apparatus of claim 1, wherein the plurality of queue heads are coupled to the frame list before any transaction descriptors during initialization of the host controller.
4. The apparatus of claim 1, wherein the plurality of queue heads are coupled to the frame list before any transaction descriptors after initialization of the host controller.
5. The apparatus of claim 1, wherein the transaction descriptors are split-isochronous transaction descriptors.
6. The apparatus of claim 1, wherein the host controller is a universal serial bus (USB) host controller.
7. The apparatus of claim 6, where the host controller is a USB 2.0 host controller.
8. A system comprising:
a first host controller and a second host controller, said first host controller capable of coupling a plurality of queue heads to a frame list, and
a device coupled to said first and second host controllers,
wherein the plurality of queue heads are coupled to the frame list before any split-isochronous transaction descriptors where split-isochronous transaction descriptors are supported.
9. The system of claim 8, further including:

a first host controller driver associated with said first host controller, and

a second host controller driver associated with said second host controller.

10. The system of claim 8, wherein the plurality of queue heads are coupled to the frame list before any transaction descriptors during initialization of the first host controller.

11. The system of claim 8, wherein the plurality of queue heads are coupled to the frame list before any transaction descriptors after initialization of the first host controller.

12. The system of claim 8, wherein the transaction descriptors are split-isochronous transaction descriptors.

13. The system of claim 8, wherein the first host controller and the second host controller are universal serial bus (USB) host controllers.

14. The system of claim 13, where the first host controller is a USB 2.0 host controller.